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November 6, 2002

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Honorable Commissioner for Patents  
Washington, D.C. 20231  
Attention: Examiner D. Nhu  
Group 2800 Before Final - Facsimile No. 703-872-9318

Re: MITANI et al., U.S. Serial No. 10/084,477  
Attorney Docket No. 501.38435VX1

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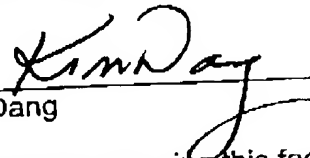
TECHNOLOGY CENTER 2800

**SUBMISSION OF AMENDMENT and PETITION FOR EXTENSION OF TIME**

Sir:  
Applicant hereby submits the attached "Amendment" (9 pgs), "Petition for Extension of Time" (1 pg), "Form PTO-2038" (1 pg) in the above-identified application.

**CERTIFICATE OF TRANSMISSION:**

I hereby certify that the attached "Amendment" (9 pgs), "Petition for Extension of Time" (1 pg), "Form PTO-2038" (1 pg) are being formally filed in the USPTO via Group 2800 BF Facsimile No. 703-872-9318 on 6 November 2002.

  
Kim Dang

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501.38435VX1

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: MITANI et al.  
Serial No.: 10/084,477  
Filed: February 28, 2002  
For: A SEMICONDUCTOR DEVICE  
Group: 2818  
Examiner: D. Nhu

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AMENDMENT

November 6, 2002

Assistant Commissioner  
for Patents  
Washington, D.C. 20231

Sir:

In response to the restriction requirement dated September 6, 2002, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as follows:

In the Claims:

Please amend claims 12 and 13 as follows:

12. (Amended) A method of driving a semiconductor device having a field effect transistor formed in a semiconductor layer provided on an insulating layer, a body electrode electrically connected to a channel forming region of said field effect transistor, and a back gate electrode provided below the insulating layer in an opposing relationship to the channel forming region of said field effect transistor, comprising:

applying a potential lying in a direction to induce an electrical charge of

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